

HD74LV1GW97A

Configurable Multiple-Function Gate

R04DS0037EJ0400 Rev.4.00 Jan 10, 2014

Description

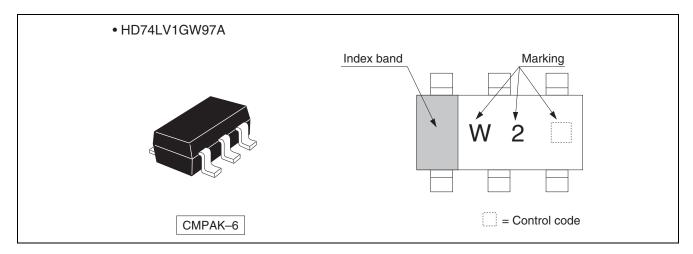
The HD74LV1GW97A has configurable multiple—function gate in a 6 pin package. The Output state is determined by eight patterns of 3-bit input. The user can choose the logic functions AND, NAND, OR, NOR, INVERTER, Non—Invert Buffer, Data Selector. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range: 1.65 to 5.5 V Operating temperature range: -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1GW97ACME	CMPAK-6 pin	PTSP0006JA-A (CMPAK-6V)	СМ	E (3,000 pcs / Reel)

Outline and Article Indication

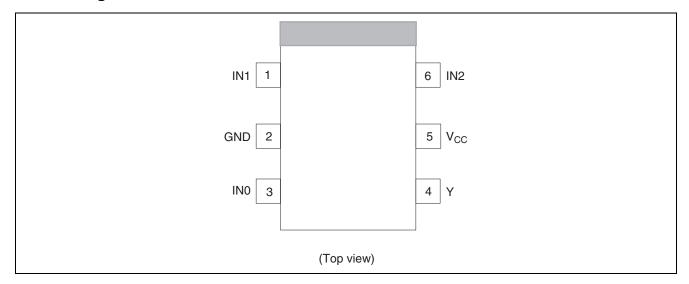


Function Table

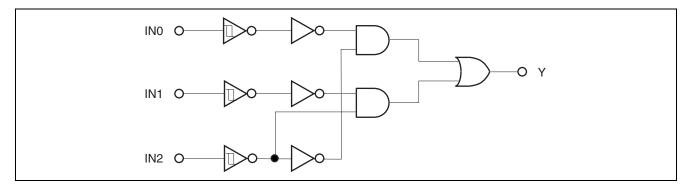
	Inputs		Output
IN2	IN1	IN0	Y
L	L	L	L
L	L	Н	L
L	Н	L	Н
L	Н	Н	Н
Н	L	L	L
Н	L	Н	Н
Н	Н	L	L
Н	Н	Н	Н

H : High level L : Low level

Pin Arrangement



Logic Diagram



Function Selection Table

Logic Function	Figure No.
2 to 1 data Selector	1
2-inputs AND	2
2-inputs OR with one input inverted	3
2-inputs NAND with one input inverted	3
2-inputs AND with one input inverted	4
2-inputs NOR with one input inverted	4
2-inputs OR	5
Inverter	6
Non–Inverter Buffer	7

Logic Configurations

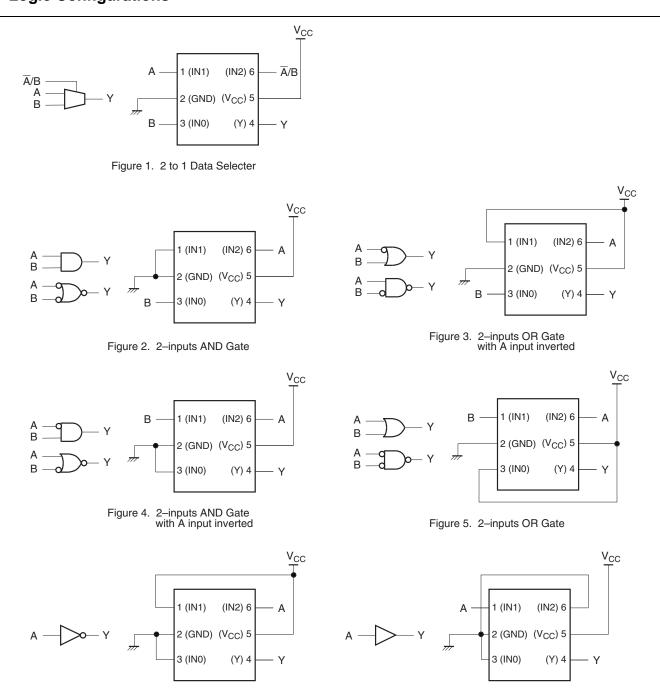


Figure 6. Inverter

Figure 7. Non-Invert Butter

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
		-0.5 to 7.0		V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	Io	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
Output current	I _{OL}	_	1	mA	V _{CC} = 1.65 to 1.95 V
		_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	12		V _{CC} = 4.5 to 5.5 V
	I _{OH}	_	-1]	V _{CC} = 1.65 to 1.95 V
		_	-2		V _{CC} = 2.3 to 2.7 V
		_	-6		V _{CC} = 3.0 to 3.6 V
		_	-12		V _{CC} = 4.5 to 5.5 V
Input transition rise or fall rate	Δt / Δν	0	300	ns / V	V _{CC} = 1.65 to 1.95 V
		0	200		V _{CC} = 2.3 to 2.7 V
		0	100		V _{CC} = 3.0 to 3.6 V
		0	20		V _{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

 $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test condition
Threshold	V _T ⁺	1.65 to 1.95	_	_	V _{CC} ×0.75	V	
voltage		2.5	_	_	1.75		
		3.3	_	_	2.31		
		5.0	_	_	3.50		
	V _T	1.65 to 1.95	V _{CC} ×0.25	_	_		
		2.5	0.75	_	_		
		3.3	0.99	_	_		
		5.0	1.5	_	_		
	ΔV_T	1.65 to 1.95	0.1	_	V _{CC} ×0.4		
		2.5	0.25	_	1.0		
		3.3	0.33	_	1.32		
		5.0	0.5	_	2.0		
Output voltage	V _{OH}	Min to Max	V _{CC} -0.1	_	_	V	$I_{OH} = -50 \mu A$
		1.65	1.4		_		$I_{OH} = -1 \text{ mA}$
		2.3	2.0	_	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_		$I_{OH} = -6 \text{ mA}$
		4.5	3.8	_	_		$I_{OH} = -12 \text{ mA}$
	V _{OL}	Min to Max	_	_	0.1		$I_{OL} = 50 \mu A$
		1.65	_	_	0.3		I _{OL} = 1 mA
		2.3	_		0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_		±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent	Icc	5.5	_		10	μΑ	$V_{IN} = V_{CC}$ or GND,
supply current							$I_0 = 0$
Output leakage	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
current							
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC} = 1.8 \pm 0.15 \text{ V}$

		Ta = 25°C		Ta = -40 to 85°C			Test	FROM	то	
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	15.8	29.4	1.0	33.0	ns	C _L = 15 pF	IN	Υ
delay time	t _{PHL}	_	22.6	40.9	1.0	45.0		C _L = 50 pF		

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$

		Ta = 25°C		Ta = -40 to 85°C			Test	FROM	то	
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	9.4	17.6	1.0	21.0	ns	C _L = 15 pF	IN	Υ
delay time	t _{PHL}		12.6	22.6	1.0	26.5		C _L = 50 pF		

 $V_{CC} = 3.3 \pm 0.3 \text{ V}$

		Ta = 25°C		Ta = -40 to 85°C			Test	FROM	то	
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.0	11.0	1.0	13.0	ns	C _L = 15 pF	IN	Υ
delay time	t _{PHL}		9.5	14.5	1.0	16.5		C _L = 50 pF		

 $V_{CC} = 5.0 \pm 0.5 \text{ V}$

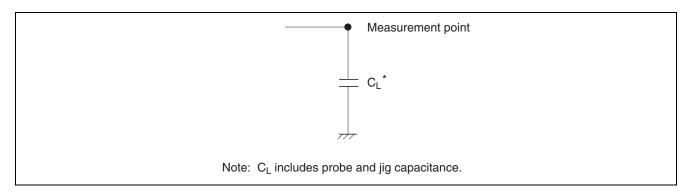
		Ta = 25°C		Ta = -40 to 85°C			Test	FROM	то	
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.8	6.8	1.0	8.0	ns	C _L = 15 pF	IN	Υ
delay time	t _{PHL}	_	6.3	8.8	1.0	10.0		C _L = 50 pF		

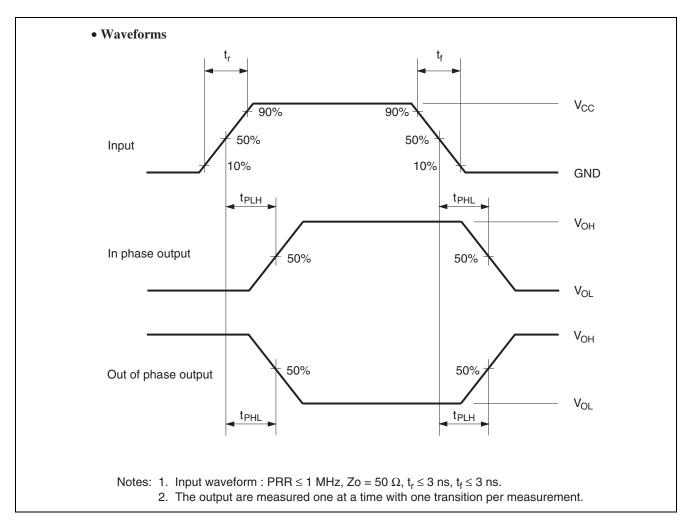
Operating Characteristics

 $C_L = 50 \text{ pF}$

			Ta = 25°C				
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation	C _{PD}	3.3	_	8.5		pF	f = 10 MHz
capacitance		5.0	_	10.0	1		

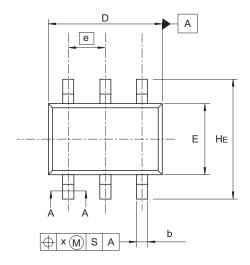
Test Circuit

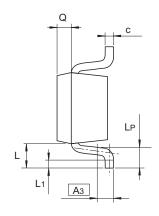


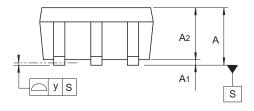


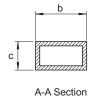
Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]	
SC-88	PTSP0006JA-A	CMPAK-6 / CMPAK-6V	0.006	









Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	0.8	_	1.1
A ₁	0	_	0.1
A ₂	0.8	0.9	1.0
A_3	_	0.25	_
b	0.15	0.2	0.25
С	0.1	0.15	0.25
D	1.8	2.0	2.2
E	1.15	1.25	1.35
е	_	0.65	_
HE	2.0	2.1	2.2
L	0.3		0.7
L ₁	0.1	_	0.5
Lp	0.2	_	0.6
Х			0.05
У			0.05
Q	_	0.25	_

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